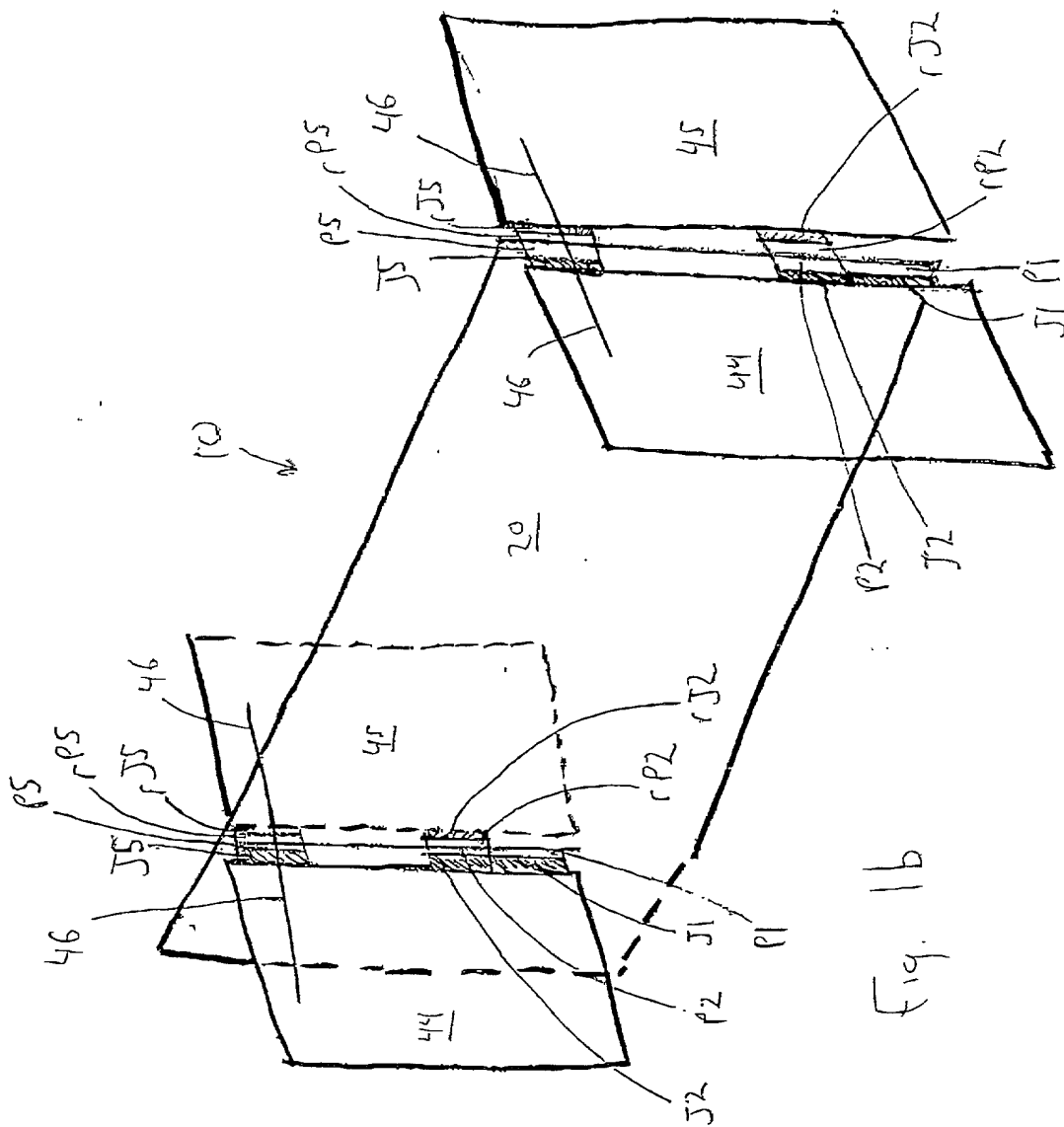


The diagram illustrates a multi-board system with three main panels: a front panel (44), a backplane (20), and a rear panel (45). The front panel (44) features a vertical stack of components labeled J1 through J5, with associated types and positions: TYPE A POS 1-251, TYPE B POS 1-191, TYPE B POS 1-221, TYPE A POS 1-251, and TYPE B POS 1-221. The backplane (20) is a central vertical strip with pins P1 through P5 and P1 through P5, with associated types and positions: TYPE A POS 1-251, TYPE B POS 1-191, TYPE B POS 1-221, TYPE A POS 1-251, and TYPE B POS 1-221. The rear panel (45) features a vertical stack of components labeled J1 through J5, with associated types and positions: TYPE A POS 1-251, TYPE B POS 1-191, TYPE B POS 1-221, TYPE A POS 1-251, and TYPE B POS 1-221. The diagram also shows right angle female receptacles, vertical male pin headers, and vertical shrouds. A large number 10 is written at the top left, and a large number 40 is written at the bottom right.

Fig. 1a



103011 2443350

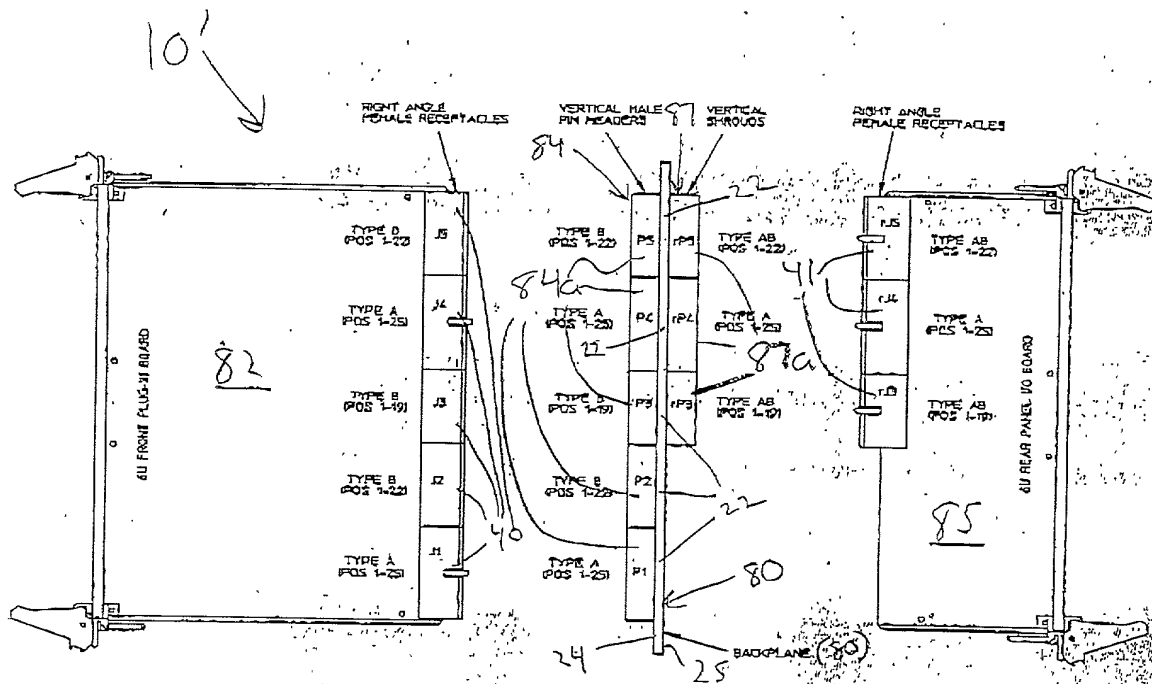
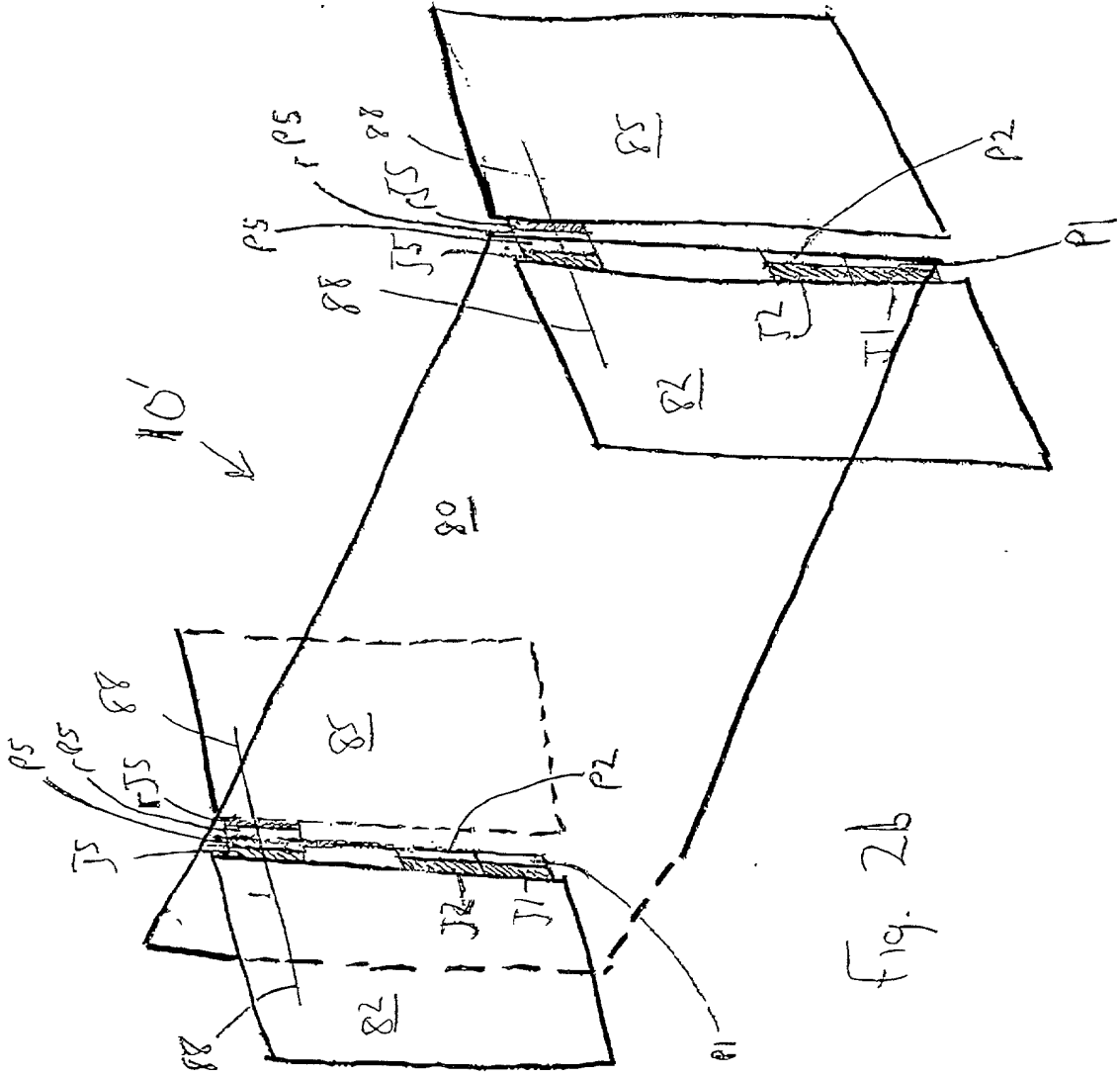


Fig. 2a



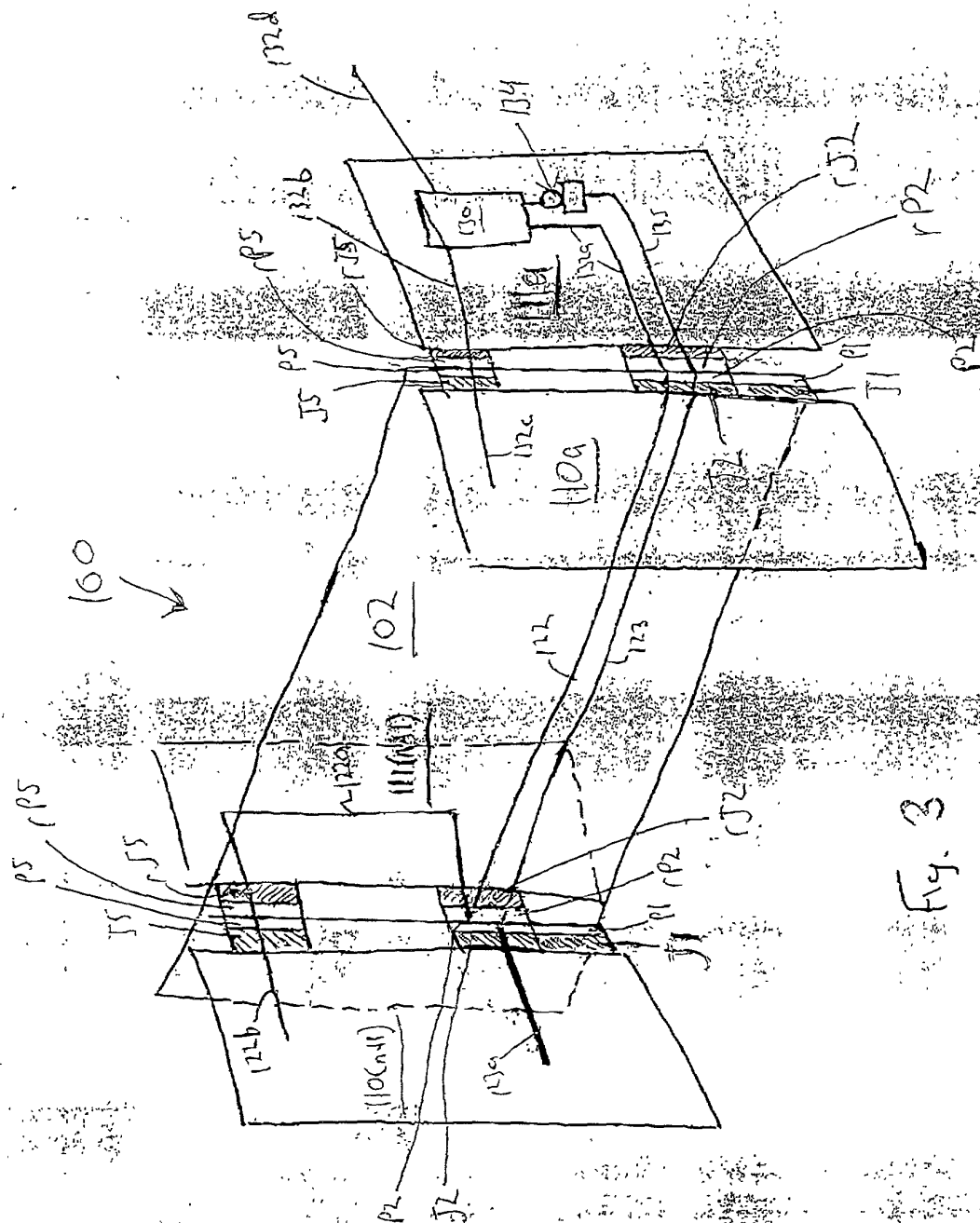


Fig. 3

103044 23433650

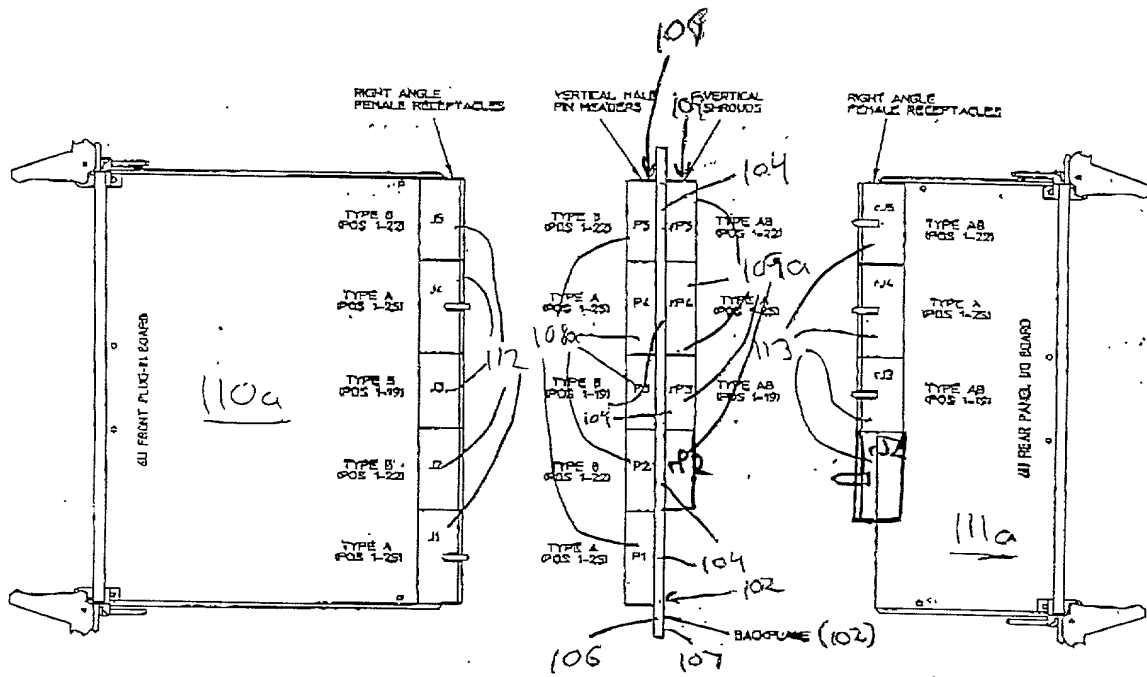


Fig. 4

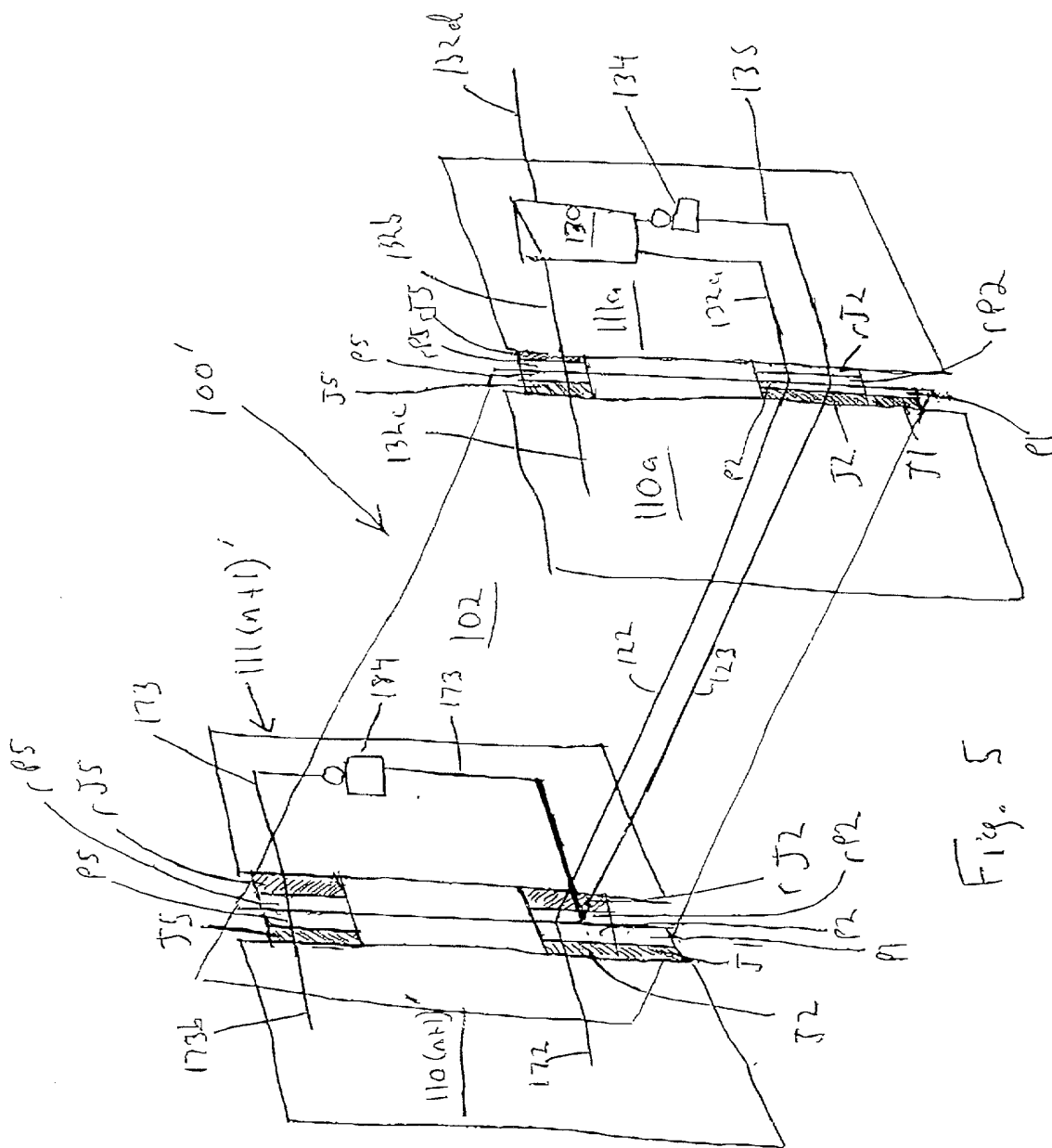


Fig 5

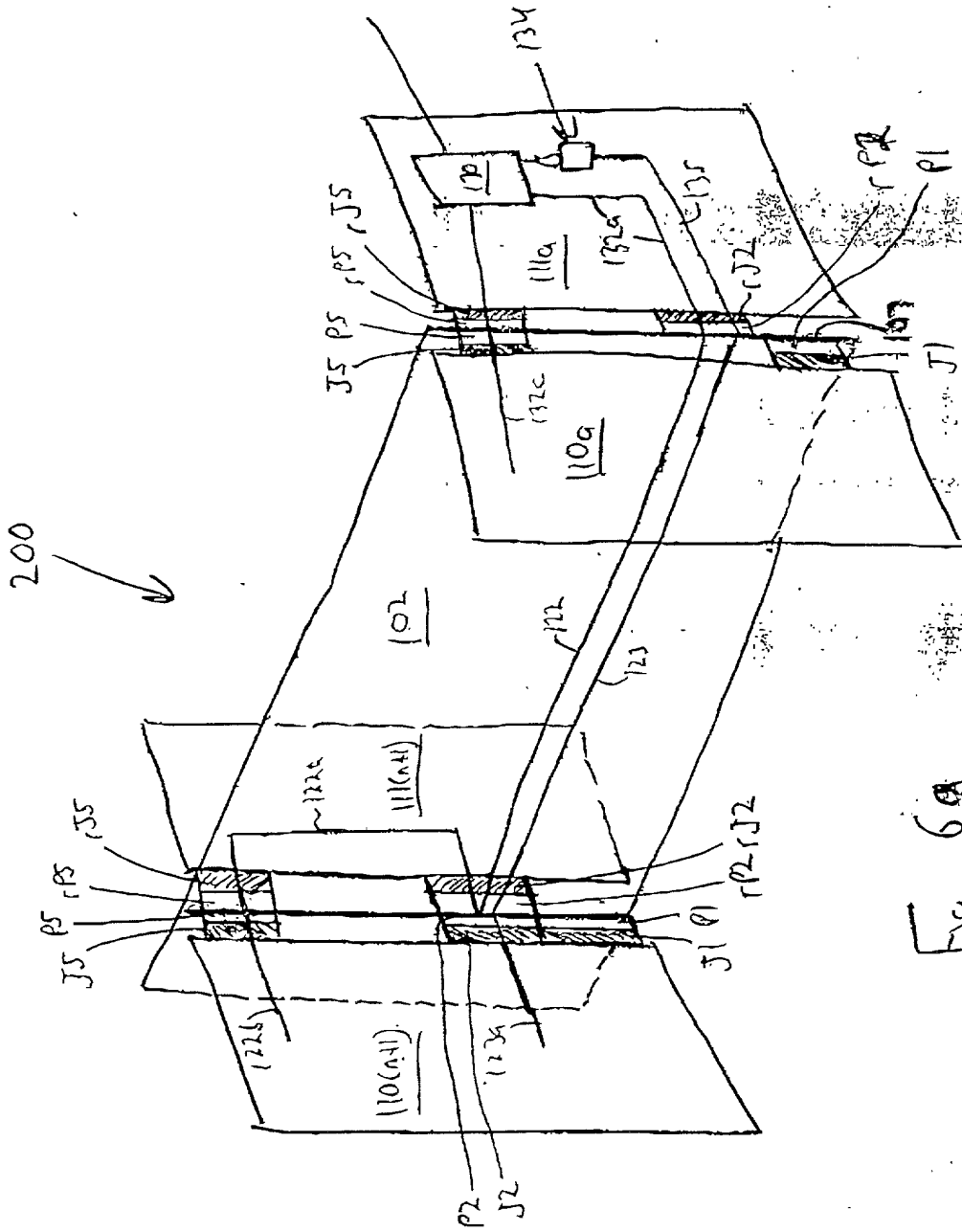


Fig. 6a






TABLE 1

## Pin Assignments

Table 13. CompactPCI Peripheral Slot 64-Bit Connector Pin Assignments<sup>(1)(10,11)</sup>

22	GND	GA4 <sup>(12)</sup>	GA3 <sup>(12)</sup>	GA2 <sup>(12)</sup>	GA1 <sup>(12)</sup>	GA0 <sup>(12)</sup>	GND	P2 / J2	
21	GND	RSV	RSV	RSV	RSV	RSV	GND		
20	GND	RSV	RSV	RSV	GND	RSV	GND		
19	GND	RSV	RSV	RSV	RSV	RSV	GND		
18	GND	BRSVP2A18	BRSVP2B18	BRSVP2C18	GND	BRSVP2E18	GND		
17	GND	BRSVP2A17	GND	RSV	RSV	RSV	GND		
16	GND	BRSVP2A16	BRSVP2B16	RSV	GND	BRSVP2E16	GND		
15	GND	BRSVP2A15	GND	RSV	RSV	RSV	GND		
14	GND	AD[35]	AD[34]	AD[33]	GND	AD[32]	GND		C O N N E C T O R
13	GND	AD[38]	GND	V(UO) <sup>(2)</sup>	AD[37]	AD[36]	GND		
12	GND	AD[42]	AD[41]	AD[40]	GND	AD[39]	GND		
11	GND	AD[45]	GND	V(UO) <sup>(2)</sup>	AD[44]	AD[43]	GND		
10	GND	AD[49]	AD[48]	AD[47]	GND	AD[46]	GND		
9	GND	AD[52]	GND	V(UO) <sup>(2)</sup>	AD[51]	AD[50]	GND		
8	GND	AD[56]	AD[55]	AD[54]	GND	AD[53]	GND		
7	GND	AD[59]	GND	V(UO) <sup>(2)</sup>	AD[58]	AD[57]	GND		
6	GND	AD[63]	AD[62]	AD[61]	GND	AD[60]	GND		
5	GND	C/BE[5]#	GND	V(UO) <sup>(2)</sup>	C/BE[4]#	PAR64	GND		
4	GND	V(UO) <sup>(2)</sup>	BRSVP2B4	C/BE[7]#	GND	C/BE[6]#	GND		
3 <sup>(2)</sup>	GND	RSV	GND	RSV	RSV	RSV	GND		
2 <sup>(2)</sup>	GND	RSV	RSV	UNC <sup>(4)</sup>	RSV	RSV	GND		
1 <sup>(2)</sup>	GND	RSV	GND	RSV	RSV	RSV	GND		
25	GND	5V	REQ64#	ENUM#	3.3V	5V	GND	P1 / J1	
24	GND	AD[11]	5V	V(UO) <sup>(2)</sup>	AD[10]	ACK64#	GND		
23	GND	3.3V	AD[4]	AD[3]	5V	AD[2]	GND		
22	GND	AD[7]	GND	3.3V	AD[6]	AD[5]	GND		
21	GND	3.3V	AD[9]	AD[8]	M66EN <sup>(9)</sup>	C/BE[0]#	GND		
20	GND	AD[12]	GND	V(UO) <sup>(2)</sup>	AD[11]	AD[10]	GND		
19	GND	3.3V	AD[15]	AD[14]	GND	AD[13]	GND		
18	GND	SERR#	GND	3.3V	PAR	C/BE[1]#	GND		
17	GND	3.3V	IPMB SCL	IPMB SDA	GND	PERR#	GND		
16	GND	DEYSEL#	GND	V(UO) <sup>(2)</sup>	STOP#	LOCK#	GND		
15	GND	3.3V	FRAME#	IRDY#	RD SEL <sup>(7)</sup>	TRDY#	GND	C O N N E C T O R	
12-14	KEY AREA								
11	GND	AD[18]	AD[17]	AD[16]	GND	C/BE[2]#	GND		
10	GND	AD[21]	GND	3.3V	AD[20]	AD[19]	GND		
9	GND	C/BE[3]#	IDSEL <sup>(8)</sup>	AD[23]	GND	AD[22]	GND		
8	GND	AD[26]	GND	V(UO) <sup>(2)</sup>	AD[25]	AD[24]	GND		
7	GND	AD[30]	AD[29]	AD[28]	GND	AD[27]	GND		
6	GND	REQ#	GND	3.3V	CLK	AD[31]	GND		
5	GND	BRSVP1A5	BRSVP1B5	RST#	GND	GNT#	GND		
4	GND	IPMB PWR	HEALTHY# <sup>(14)</sup>	V(UO) <sup>(2)</sup>	INTP	INTS	GND		
3	GND	INTA#	INTB#	INTC#	3.3V	INTD#	GND		
2	GND	TCK <sup>(13)</sup>	5V	TMS <sup>(15)</sup>	TDO <sup>(16)</sup>	TDI <sup>(16)</sup>	GND		
1	GND	5V	-12V	TRST# <sup>(15)</sup>	+12V	5V	GND		
Pin	Z <sup>(14)</sup>	A	B	C	D	E	F <sup>(9)</sup>		

 = long pins (front only)

 = short pins (front only)


 = medium length pins (front only)

Fig. 7

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